

Sensor Connectivity – VA70XX

The Valens VA70XX chipset family is the first to implement the global MIPI A-PHYSM standard, offering multi-gig asymmetric connectivity. The chipsets are optimized for CSI-2-based sensors – cameras, radars, and LiDARs, simplifying in-vehicle architecture and reducing total system cost.



VA7044

- Deserializer for the extension of up to four CSI-2 channels over MIPI A-PHY, for up to 15m/50ft, with support of 4 x 2-8Gbps, and additional input of a local D-PHY CSI-2 link
- Output of 2 X CSI-2 links, each to be configured as C-PHY or D-PHY
- Support for up to 16 CSI-2 virtual channels on each CSI-2 link; each can be routed to any output, including duplication



VA7042

- Deserializer for the extension of up to two CSI-2 channels over MIPI A-PHY, for up to 15m/50ft, with support of 2 x 2-8Gbps, and additional input of a local D-PHY CSI-2 link
- Output of 2 X CSI-2 links, each to be configured as C-PHY or D-PHY
- Support for up to 16 CSI-2 virtual channels on each CSI-2 link; each can be routed to any output, including duplication



VA7031

- Serializer for input and extension of a CSI-2 channel over MIPI A-PHY, for up to 15m/50ft
- Support for bandwidth of 2-8Gbps, with up to four inline connectors
- Support for up to 16 CSI-2 virtual channels on the CSI-2 link



VA7021

- Cost-optimized serializer for input and extension of a CSI-2 channel over MIPI A-PHY, for up to 15m/50ft
- Support for bandwidth of 2-4Gbps, with up to four inline connectors
- Support for up to 16 CSI-2 virtual channels on the CSI-2 link