

# GW2A/2AN

GOWIN's Arora family of SRAM-based FPGA deliver an unparalleled performance-to-cost ratio, positioning it as the optimal choice for compute-intensive applications for industrial, automotive, consumer, medical and renewable energy applications.



| Device                    | GW2AN-9X  | GW2AN-18X | GW2AN-55 |
|---------------------------|-----------|-----------|----------|
| LUT4                      | 10,368    | 20,736    | 54,720   |
| S-SRAM(bits)              | 40K       | 40K       | 106K     |
| B-SRAM(bits)              | 540K      | 540K      | 2,520K   |
| NOR Flash                 | 16M bit   | 16M bit   | 32M      |
| PLLs                      | 2         | 2         | 6        |
| Global Clock              | 8         | 8         |          |
| High Speed Clock          | 8         | 8         |          |
| LVDS (Mbps)               | 1250      | 1250      |          |
| MIPI (Mbps)               | 1200      | 1200      |          |
| Total number of I/O banks | 9         | 9         | 8        |
| Max User IO               | 389       | 389       | 608      |
| Core voltage(LV)          | 1.0V      | 1.0V      | 1.0V     |
| Core voltage(EV)          | 1.2V      | 1.2V      |          |
| Core voltage(UV)          | 2.5V/3.3V | 2.5V/3.3V |          |