

GW5AT

The Arora V FPGA series by GOWIN offers SRAM-based FPGA up to 138k LUTs, featuring high-speed SerDes, Hardcore PCIe, Hardcore MIPI and advanced SEU capabilities on a 22nm low-power TSMC process.



Device	GW5AT-15	GW5AT-60	GW5AT-75	GW5AT-138
LUT4	15,120	59,904	86,688	138,240
S-SRAM (kb)	118,125	468	677	1,080
B-SRAM (kb)	630	2,124	4608	6,120
DSP	35	118	256	298
PLLs	2	8	12	12
Global Clock	16	16	16	16
HCLK	8	20	24	24
Transceivers	4	4	8	8
Transceivers Rate	270Mbps -10Gbps	270Mbps -12.5Gbps	270Mbps -12.5Gbps	270Mbps -12.5Gbps
PCIe 2.0	1, x1, x2, x4		1, x1, x2, x4, x8	
LVDS Gbps	1.25	1.25	1.25	1.25
DDR3 Gbps	1.333	1.333	1.333	1.333
MIPI DPHY Hardcore	2.5Gbps(RX/TX) 4 Data Lanes 1 Clock Lanes	2.5Gbps(RX/TX) 4 Data Lanes 1 Clock Lanes	2.5Gbps(RX/TX) 4 Data Lanes 1 Clock Lanes	2.5Gbps(RX/TX) 4 Data Lanes 1 Clock Lanes
MIPI CPHY Hardcore	2.5Gbps 3-trios data lanes	2.5Gbps 3-trios data lanes	-	-
ADC	1	2	2	2
GPIO Banks	4	11	6	6
Max User IO	52	320	312	312
Core Voltage	0.9V/1.0V	0.9V/1.0V/1.2V	0.9V/1.0V	0.9V/1.0V