

REAL TIME CLOCK MODULE (I²C-Bus)

Time stamp function and Low current consumption





Product Number (2,000 pcs / Reel) RX8111CE A: X1B000421000115 RX8111CE B: X1B000421000215

RX8111CE

• Built in frequency adjusted 32.768 kHz crystal unit Interface Type : I2C -Bus Low backup current : 100 nA Typ. / 3 V

· Auto power switching function : Automatically switches to backup power

supply by monitoring the VDD voltage

 Time stamp function : 8 times stamped from year to 1/256 seconds • Interrupt output : Wake up every minute or every second Alarm interruption : Day, date, hour, minute, second

Auto repeat wakeup timer interruption

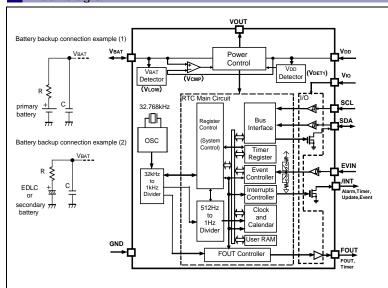
• Self-monitoring interruption : Crystal oscillation stop, V_{BAT} low, V_{DD} low

The I²C-Bus is a trademark of NXP Semiconductors

RX8111CE

 $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$

Block diagram



Overview

Interface type

I²C-Bus interface Fast-Mode 400 kHz

Auto power switch function

The \dot{V}_{DD} voltage is monitored and it switches to the backup power supply by the automatic operation

Backup power supply switching voltage 1.2 V Min.

• Clock output function

Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz When the clock output is not used, the FOUT pin can be used as

a timer output pin (CMOS) Wakeup timer function

Selectable from 244 µs to 32 years (24 bit x 1 ch.) Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz Auto release after interrupt output from /INT pin at timer completes

This operation is auto repeat with a selected cycle, it can be used like a watchdog timer

. Time stamp function

8 times stamped from year to 1/256 seconds

The time stamp trigger inputs from EVIN pin, self-monitoring and I²C software command

EVIN pin has function of chattering-cancel

Alarm function

It is possible program from year to second

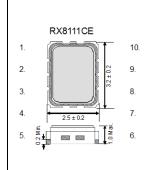
· Self-monitoring interruption

Crystal oscillation stop, VBAT low, VDD low

Pin Functin

Signal Name	1/0	Function
EVIN	Input	External event input pin (Pull up/down and polarity are selectable by software)
SCL	Input	Serial clock input pin
SDA	Input / Output	Serial data input and output pin
		Frequency output pin (CMOS) (frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)
/INT	T Output Interrupts output by Alarm and Timer events (N-ch. open drain)	
VDD	-	Power-supply pin Possible to supply different voltage from Vio
Vio	-	Interface power supply pin Input to supply the voltage same as a host
Vout	-	Internal voltage output pin Connect bypass capacitor of 1.0 μF
VBAT	-	This is a power supply pin for backup battery Connect an EDLC, a secondary battery, a primary battery In the backup voltage range, supplied to IC, from this pin
GND	-	Ground pin

Terminal connection / External dimensions (Unit: mm)



Pin	Connection				
1	VDD				
2	Vout				
3	VBAT				
4	FOUT				
5	SCL				
6	EVIN				
7	SDA				
8	Vio				
9	GND				
10	/INT				

Specifications (characteristics)

■ Recommended Operating Conditions						
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Operating supply voltage	VDD	•	1.6	3.0	5.5	٧
Clock supply voltage	Vclk	-	1.1	3.0	5.5	V
Operating temperature	Ta	-	-40	+25	+85	°C
VDD detect voltage	-VDET1	VDD, Fall	1.20	1.40	1.60	٧
Transport of an atomistics						

■ Frequency characteristics							
Item	Grade	Symbol	Conditions	Min.	Тур.	Max.	Unit
Frequency tolerance	Α	Δf/f	Ta = +25 °C VDD = 3.0 V	-11.5	-	+11.5	x 10 ⁻⁶
Frequency tolerance	В			-23	-	+23	
Oscillation start-up time		tsta	VDD = 2.75 V to 5.5 V	-	0.3	1.0	s

* Refer to application manual for details

■ Curren	Current consumption characteristics				Ta = -40 °C to +85 °C			
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit		
Current	IBAT	SCL = SDA = " L", FOUT = OFF, /INT = OFF, VBAT = 3.0 V, VDD = VIO = 0.0 V, CHGEN = 0b, INIEN = 0b, SWSEL0 = 1, SWSEL1 = 0	-	100	450	nA		
 consumption	l32k	FOUT = 32.768 kHz, /INT = OFF, VDD = VIO = 3.0 V, FOUT pin CL = 15 pF	-	2.0	3.0	μА		

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▶ Complies with EU RoHS directive.

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