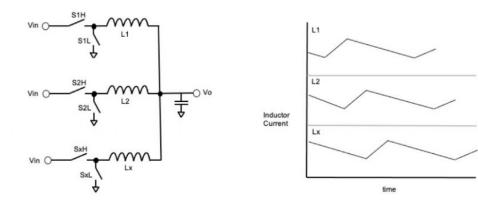


## DUAL WINDING TLVR INDUCTORS FOR SERVER VCORE POWER, 100NH TO 180NH, 117APK



Pulse continues to expand our portfolio of inductors used to power processors, memory, FPGAs and ASICs in servers, datacenters and storage systems. Our latest product developments are specifically designed for use with the trans-inductor voltage regulator (TLVR) topology and leverage our existing high volume automated manufacturing to ensure quality, reliability, and cost-effectiveness. The TLVR topology enables faster transient response and utilizes a dual winding power bead inductor.

Traditionally, these applications have used single winding inductor used in a multiphase buck topology which allows currents to be distributed though parallel 'paths' or phases. By operating each phase in a staggered fashion, the ripple currents in each phase tend to cancel at the output and the overall ripple current seen at the load is much smaller than the ripple on each phase. This approach allows smaller inductances to be used (typically on the order of 50-300nH) and can allow design engineers a trade-off between how quickly the converter responds to a change in the load current (transient response) verses stability of the control loop. By adjusting the number of phases used in the converter, the designer has flexibility to optimize the circuit for their specific application.





Singel 3 | B-2550 Kontich | Belgium | Tel. +32 (0)3 458 30 33 info@alcom.be | www.alcom.be Rivium 1e straat 52 | 2909 LE Capelle aan den IJssel | The Netherlands Tel. +31 (0)10 288 25 00 | info@alcom.nl | www.alcom.nl However, as processors and FPGA's demand faster and larger current transients the voltage regulators used must increase their bandwidth to improve on response time. This is usually done by either increasing the number of phases and/or reducing the inductance value of each phase. These solutions create complications in the converter design. Increasing the phase count necessitates less space per phase which means less 'width' for the power stages and inductors. For the inductor, this space constraint creates 'skinny' but tall components that can become mechanically unstable which limits the ability to shrink the inductor width. Reducing the inductance value used per power stage (which is already sub 100nH) drives higher ripple currents per phase resulting in higher AC losses as well as complicating the converter control. It is important to note that transient response time of the entire converter is somewhat limited by the fact that each phase will respond to the step by increasing or decreasing its duty cycle in a round-robin fashion, one at a time.

In order to mitigate these issues and further drive performance improvements, a "Trans-Inductor" based voltage regulator was developed in the technical disclosure commons domain. The trans-inductor is very similar to a traditional one turn VR inductor, but has the addition of a second tightly coupled winding. The main low DCR 'primary' is used in the same fashion as in a traditional VR inductor, having a PWM input with several phases feeding the single output. Each single turn 'secondary' is tied in a series loop referenced to the local ground. When a load step occurs on a given phase, the resultant increase or decrease in duty cycle is reflected to its secondary winding and because the secondaries of each phase are in a series loop they all 'see' this change immediately. Through the magnetic coupling this 'sensed' change is reflected to each inductors primary winding. In effect, this allows all phases to respond simultaneously to a load transient greatly reducing the time needed for the converter to adjust.

Additionally, a compensation inductor has been introduced into the converter circuit. It is typical for this inductor to be similar in construction to a traditional VR inductor. The compensation inductor exists to give design engineers the ability to adjust the total inductance in the secondary loop, allowing for freedom to adjust for converter stability. Due to the AC coupled nature of the secondary circuit, the compensation inductor is subject to a superposition of each phases ripple current. In practice, this means that for a 4 phase converter switching at 1MHz, the compensation inductor will see a reflected ripple current with a frequency of 4MHz. At such frequencies care must be taken to minimize AC winding loss as well as ensuring a proper high frequency, low loss core material is chosen.



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