

REAL TIME CLOCK MODULE (SPI)

For Automotive, Built-in 32.768 kHz DTCXO,









Product Number (2,000 pcs / Reel)

RA4000CE YB A0 : X1B000491A00115 RA4000CE YB B8: X1B000491A00915 RA4000CE YB C0: X1B000491A01015 RA4000CE YB D0: X1B000491A01115 RA4000CE YB E8: X1B000491A01915

RA4000CE

Built in frequency adjusted 32.768 kHz crystal unit and DTCXO

• Interface Type : 3 wire / 4 wire SPI-Bus

· Time stamp function : 2 time stamps from year to second

· Reset functions with a delay : Output a reset signal when a VDD voltage drop status

is detected.

 Interrupt output : Wake up every minute or every second · Alarm interruption : Day, date, hour, minute, second

Auto repeat wakeup timer interruption

: Crystal oscillation stop, V_{DD} low Self-monitoring interruption

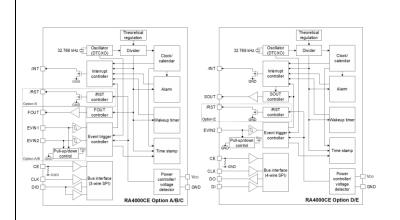
AEC-Q100 compliant



RA4000CE

 $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$

Block diagram



Overview

- Interface type
- 3 wire / 4 wire SPI-Bus
- High stability
- $\pm\,5.0$ x 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds) $\pm\,8.0$ x 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds) $\pm\,50.0$ x 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)
- Clock output function

Selectable from 32.768 kHz, 1024 Hz and 1 Hz outputs

Wakeup timer function

Selectable from 976.56 µs to 32 years cycle

Can be used as a time integration meter

Can be used like a watchdog timer

Time stamp function

Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years

Number of recordable events: 2 events

Trigger source: External event (EVIN) input, voltage drop/oscillation

stop status detected, command input from the host

EVIN pin has function of chattering-cancel

· Reset function with a delay

Can output a reset signal when a VDD voltage drop status is detected

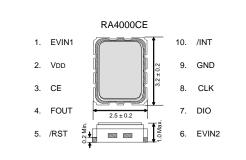
Status output (SOUT)

Can output the selected internal flag (interrupt flag, voltage drop detection flag) status.

Pin Function

| Signal Name | 1/0 | Function | | | | |
|-------------|-------------------|---|--|--|--|--|
| EVIN1, 2 | Input | External event input pins Pull-up and pull-down is configurable by the resisters | | | | |
| CE | Input | Slave select input pin A pull-down resistor (Typ. 300 k Ω) is included | | | | |
| CLK | Input | Serial clock input pin | | | | |
| DI | Input | Serial data input pin (4 wire) | | | | |
| DO | Output | Serial data Output pin (4 wire) | | | | |
| DIO | Input / Output | Serial data input/output pin (3 wire) | | | | |
| FOUT | Output | Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS) | | | | |
| /INT | Output | Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output. | | | | |
| /RST | Output | Reset output pin (N-ch. open drain) | | | | |
| SOUT | Output | Status output pin | | | | |
| Vdd | - | Power-supply pin | | | | |
| GND | - | Ground pin | | | | |

Terminal connection / External dimensions (Unit: mm)

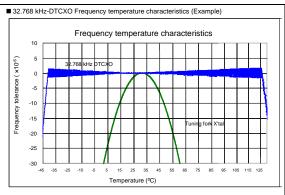


*The above diagram is the terminal layout for Option B. For other options, please refer to the Pin Option section.

Specifications (characteristics)

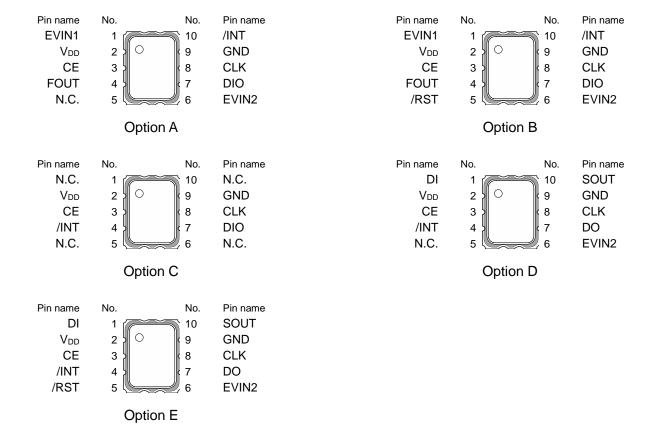
| ■ Electrical Characteristics | | | | | | | | | | | |
|------------------------------|-------------------|------------------------------------|-------------------------------------|----------|----------------|-------|------|--------------------|----|--|--|
| Item | Symbol | nbol Conditions | | | Min. | Тур. | Max. | Unit | | | |
| Operating voltage | VDD | - | | | 1.6 | 3.0 | 5.5 | V | | | |
| Temp. compensated Voltage | Vтем | VTEM - | | | | 1.6 | 3.0 | 5.5 | V | | |
| Clock supply voltage | Vclk | - | | | 1.3 | 3.0 | 5.5 | V | | | |
| Operating temperature | Ta - | | | | -40 | +25 | +125 | °C | | | |
| | | | $T_a = -40$ °C to +85 °C | | ±5.0 | | | | | | |
| Frequency tolerance | Δf/f | YB | Ta = +85 °C to +105 °C | | ±8.0 | | | x 10 ⁻⁶ | | | |
| | | | T _a = +105 °C to +125 °C | | | ±50.0 | | | | | |
| | I _{DD1} | /INT = Hi-Z, FOUT: | | No | $V_{DD} = 5 V$ | 1 | 0.35 | 1.8 | | | |
| Current consumption | IDD2 | | OFF (Hi-Z), | /RST pin | $V_{DD} = 3 V$ | - | 0.3 | 1.7 | μА | | |
| Current consumption | I _{DD11} | Temp. Compensation interval 2.0 s, | | With | $V_{DD} = 5 V$ | 1 | 1.5 | 3.7 | μА | | |
| | I _{DD12} | CE = L | | /RST pin | $V_{DD} = 2 V$ | - | 0.6 | 2.25 | | | |

* Refer to application manual for details



Pin Option

| | Pin name | | | | | | | | |
|---------|----------|------------|----------|----------|----------|--|--|--|--|
| Pin No. | Option A | Option B | Option C | Option D | Option E | | | | |
| | | 3 wire | 4 wire | | | | | | |
| 1 | EVI | N1 | N.C. | DI | | | | | |
| 2 | VDD | | | | | | | | |
| 3 | CE | | | | | | | | |
| 4 | FO | UT | /INT | | | | | | |
| 5 | N.C. | /RST | N. | N.C. /RS | | | | | |
| 6 | EVI | IN2 | N.C. | EVIN2 | | | | | |
| 7 | DIO DO | | | | | | | | |
| 8 | CLK | | | | | | | | |
| 9 | GND | | | | | | | | |
| 10 | /IN | I T | N.C. | SOUT | | | | | |



Product name

RA4000CE YB A 0 1 2 34

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- 2 Frequency tolerance

YB: $\pm 5.0 \times 10^{-6}$ / $\pm 40 \,^{\circ}$ C to $\pm 85 \,^{\circ}$ C (Monthly rate: $\pm 13.2 \,^{\circ}$ seconds) $\pm 8.0 \times 10^{-6}$ / $\pm 85 \,^{\circ}$ C to $\pm 105 \,^{\circ}$ C (Monthly rate: $\pm 21 \,^{\circ}$ seconds) $\pm 50.0 \times 10^{-6}$ / $\pm 105 \,^{\circ}$ C to $\pm 125 \,^{\circ}$ C (Monthly rate: $\pm 132 \,^{\circ}$ seconds)

- 3 Pin Option
 - A: Option A
 - B: Option B
 - C: Option C
 - D: Option D
 - E: Option E
- 4 Reset output function
 - 0: No /RST pin
 - 8: With /RST pin (VDD drop detection voltage: +2.4 V Typ.)

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