TPT1051Q, TPT1051VQ

Automotive Fault Protected High-Speed CAN FD Transceiver with Silent Mode

Features

- Meets the ISO 11898-2:2016 and SAE J2284-1 to SAE J2284-5 Physical Layer Standards
- · Supports CAN FD and Data Rating up to 5 Mbps
- Short Propagation Delay Times and Fast Loop Times
- 5-V Power Supply, I/O Voltage Range Supports 2.8-V to 5.5-V MCU Interface
- Ideal Passive Behavior to CAN Bus when Unpowered
- Common-Mode Input Voltage: ±30 V
- · Protection Feature:
 - IEC 61000-4-2 ESD Protection up to ±15 kV
 - Bus Fault Protection: ±70 V
 - VCC and VIO (V variants only) Under-voltage Protection
 - TXD Dominant Time-out Function and Bus-Dominant Time-out Function
 - Thermal Shutdown Protection
- Available in SOP8 Package and Leadless DFN3X3 Package
- AEC-Q100 Qualified for Automotive Applications , Grade 1

Applications

- All Devices Supporting Highly Loaded CAN Networks
- Automotive and Transportation
 - Body Electronics / Lighting
 - Power Train / Chassis
 - Infotainment / Cluster
 - ADAS / Safety

Description

The TPT1051 is a CAN transceiver that meets the ISO11898 high-speed CAN (Controller Area Network) physical layer standard. The device is designed to be used in CAN FD networks up to 5 Mbps, with enhanced timing margin and higher data rates in long and highly loaded networks. As designed, the device features crosswire, overvoltage, and loss of ground protection from -70 V to +70 V, over-temperature shutdown, with a -30 V to +30 V common-mode input voltage range. The TPT1051V has a secondary power supply input for I/O level shifting the input pin thresholds and RXD output level. This family has a silent mode which is also commonly referred to as the listen-only mode. Additionally, all devices include many protection features to enhance the device and network robustness.

The TPT1051 and TPT1051V are available in SOP8 and DFN3X3-8L packages and are characterized from -40° C to $+125^{\circ}$ C.

Typical Application Circuit

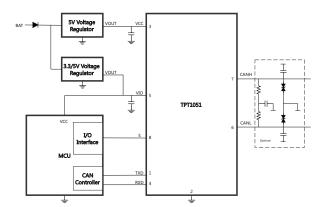




Table of Contents

Features	1
Applications	1
Description	1
Typical Application Circuit	1
Product Family Table	3
Revision History	3
Pin Configuration and Functions	4
Specifications	6
Absolute Maximum Ratings	6
ESD(Electrostatic Discharge Protection)	6
Recommended Operating Conditions	7
Thermal Information	7
Electrical Characteristics	8
AC Timing Requirements	12
Detailed Description	14
Overview	14
Functional Block Diagram	14
Feature Description	
Application and Implementation	17
Application Information	
Typical Application	17
Tape and Reel Information	18
Package Outline Dimensions	19
DFN3X3-8	19
SOP-8	20
Order Information	21



Product Family Table

Order Number	VCC (V)	VIO (V)	BUS Protection (V)	Package
TPT1051VQ-SO1R-S	4.5 to 5.5	2.8 to 5.5	±70	SOP8
TPT1051VQ-DF6R-S	4.5 to 5.5	2.8 to 5.5	±70	DFN3X3-8L
TPT1051Q-SO1R-S	4.5 to 5.5	2.8 to 5.5	±70	SOP8
TPT1051Q-DF6R-S	4.5 to 5.5	2.8 to 5.5	±70	DFN3X3-8L

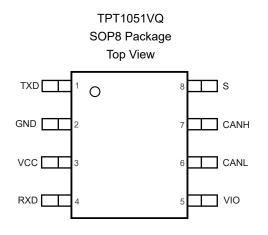
Revision History

Date	Revision	Notes
2020-12-05	Rev.Pre.0	Initial Version.
2022-12-21	Rev.A.0	Released Version

www.3peak.com 3 / 21 CA20230206A0



Pin Configuration and Functions



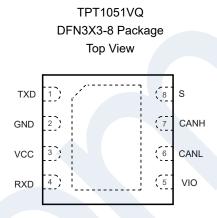
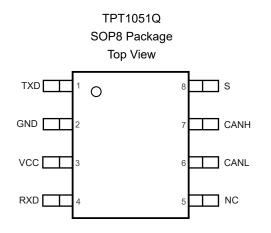


Table 1. Pin Functions: TPT1051VQ

Р	in		
No.	Name	1/0	Description
1	TXD	1	CAN transmit data input (LOW for dominant and HIGH for recessive bus states)
2	GND	GND	Ground
3	VCC	POWER	Transceiver 5 V supply voltage
4	RXD	О	CAN receive data output (LOW for dominant and HIGH for recessive bus states)
5	VIO	POWER	Transceiver I/O level shifting supply voltage
6	CANL	BUS I/O	Low level CAN bus input/output line
7	CANH	BUS I/O	High level CAN bus Input/output line
8	S	1	Silent (listen-only) mode , Mode control (Active High)

www.3peak.com 4 / 21 CA20230206A0





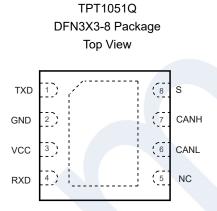


Table 2. Pin Functions: TPT1051Q

Р	in	1/0	Description
No.	Name	I/O	Description
1	TXD	1	CAN transmit data input (Low for dominant and High for recessive bus states)
2	GND	GND	Ground
3	VCC	Power	Transceiver 5 V supply voltage
4	RXD	0	CAN receive data output (Low for dominant and High for recessive bus states)
5	NC	-	Not Connected
6	CANL	Bus I/O	Low-level CAN bus input/output line
7	CANH	Bus I/O	High-level CAN bus input/output line
8	S	I	Silent (listen-only) mode , Mode control (Active High)

www.3peak.com 5 / 21 CA20230206A0



Specifications

Absolute Maximum Ratings

	Parameter	Min	Max	Unit
Vcc	5-V Bus Supply Voltage Range	-0.3	7	V
V _{IO}	I/O Level-Shifting Voltage Range	-0.3	7	V
V _{BUS}	Can Bus I/O Voltage Range (CANH, CANL)	-70	70	V
V _{BUS_DIFF}	Differential Voltage of Can Bus, CANH - CANL	-70	70	٧
V _{LOGIC}	Logic Input And Output Terminal Voltage Range (TXD, STB, RXD)	-0.3	7	٧
I _{O_RXD}	Rxd (Receiver) Output Current	-8	8	mA
TJ	Maximum Junction Temperature	-40	150	°C
T _{STG}	Storage Temperature Range	-65	150	ô
Тотр	Shutdown Junction Temperature		170	°C

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

ESD(Electrostatic Discharge Protection)

	Parameter	Condition	Minimum Level	Unit
IEC	IEC Contact Discharge	IEC-61000-4-2, Bus Pin	±15	kV
IEC	IEC Air-Gap Discharge	IEC-61000-4-2, Bus Pin	±15	kV
НВМ	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001, CAN Bus Pin	±8	kV
		ANSI/ESDA/JEDEC JS-001, All Pin	±8	kV
CDM	Charged Device Model ESD	Charged Device Model ESD ANSI/ESDA/JEDEC JS-002, All Pin		kV
LU	Latch up	Latch up per JESD78, All Pin (3)	±500	mA
		Pulse 1	-100	V
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	ISO7637-2 transients per IEC	Pulse 2a	75	V
V _{TRAN}	62228-3, CANH, CANL	Pulse 3a	-150	V
		Pulse 3b	100	V

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

www.3peak.com 6 / 21 CA20230206A0

⁽²⁾ This data was taken with the JEDEC low effective thermal conductivity test board.

⁽³⁾ This data was taken with the JEDEC standard multilayer test boards.

⁽²⁾ JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

⁽³⁾ The test is at the temperature of 25°C.



Recommended Operating Conditions

	Parameter	Min	Max	Unit
V _{IO}	Input/output voltage	2.8	5.5	V
Vcc	Power Supply	4.5	5.5	V
I _{OH(RXD)}	RXD Terminal High-Level Output Current	-2		mA
I _{OL(RXD)}	RXD Terminal Low-Level Output Current		2	mA
T _A	Operating Ambient Temperature	-40	125	°C

Thermal Information

Package Type	θυΑ	θυς	Unit
SOP8	118	48	°C/W
DFN3x3-8	51	23	°C/W

www.3peak.com 7 / 21 CA20230206A0



Electrical Characteristics

All test conditions: V_{CC} = 4.5 V to 5.5 V, R_L = 60 Ω , T = -40°C to 125°C, unless otherwise noted.

	Parameter	Conditions	Min	Тур	Max	Unit
Pin V _{CC} , (P	ower supply)					
Vcc	Supply Voltage		4.5	-	5.5	V
Vuv_vcc	Rising Undervoltage Detection on V_{cc} for Protected Mode		-	4.1	4.5	V
Vuv_vcc	Falling Undervoltage Detection on V_{cc} for Protected Mode		3.6	3.9	-	V
V _{HYS_UVVCC}	Hysteresis Voltage on UV _{VCC} ⁽¹⁾		-	200	-	mV
Icc	Silant Mada Sunnhy Cumant	1051V, device with the "V" suffix, Silent Mode, V_{TXD} = V_{CC} , RL = 60 Ω , C_L = open, V_S = V_{CC}	0.5	1.0	2.5	mA
	Silent Mode Supply Current	1051, device without the "V" suffix, Silent Mode, $V_{TXD} = V_{IO}$, RL = 60 Ω , C_L = open, $V_S = V_{IO}$	0.5	1.0	2.5	mA
	Normal Mode Supply Current	Recessive, $V_{TXD} = V_{CC}$, R_L = 50 Ω , C_L = open, V_S = 0 V	0.5	1.0	2.5	mA
		Dominant, $V_{TXD} = 0 \text{ V}$, $R_L = 60 \Omega$, $C_L = \text{open}$, $V_S = 0 \text{ V}$	20	50	80	mA
		Dominant bus fault, short circuit on bus lines, V_{TXD} = V_S = 0 V, -3 V < (V_{CANH} = V_{CANL}) < +18V, R_L = C_L = open	2	60	110	mA
PIN V ₁₀ , (I/0	D Level Adapter Supply) ⁽²⁾					
V _{IO}	Supply Voltage on V _{IO} Pin		2.8	-	5.5	V
V	Rising Undervoltage Detection on V _{IO} For Protected Mode		-	2	2.7	V
Vuv_vio	Falling Undervoltage Detection on V _{IO} For Protected Mode		1.3	1.9	-	V
V _{HYS_UVVIO}	Hysteresis Voltage on UV _{VIO} for Protected Mode		-	100	-	mV
5_57710	Silent Mode Supply Current on Pin V _{IO}	Silent Mode, RXD Floating, V _{TXD} = V _S = V _{IO}	-	10	16	μA
l _{IO}	Normal Mode Supply Current on Pin V _{IO}	Recessive, $V_S = 0 V$, $V_{TXD} = V_{IO}$	-	15	30	μA
		Dominant, V _S = 0 V,	-	200	600	μA

www.3peak.com 8 / 21 CA20230206A0



	Parameter		Conditions	Min	Тур	Max	Unit
			V _{TXD} = 0 V				
Pin S, (Sil	ent Mode Control Input)						
V _{IH}	High-Level Input Voltage			0.7 x V _{IO}	-	V _{IO} + 0.3	V
VIL	Low-Level Input Voltage			-0.3	-	0.3 x V _{IO}	٧
I _{IH}	High-Level Input Current		V _S = V _{CC} = V _{IO} = 5.5 V	8	11.8	15	μA
I _{IL}	Low-Level Input Current		$V_{S} = 0 \text{ V},$ $V_{CC} = V_{IO} = 5.5 \text{ V}$	-1	0	1	μA
I _{LKG_OFF}	Unpowered Leakage Current		$V_S = 5.5 \text{ V},$ $V_{CC} = V_{IO} = 0 \text{ V}$	-1	0	1	μA
Pin TXD, (CAN Transmit Data Input)						
V _{IH}	High-Level Input Voltage			0.7 x V _{IO}	-	V _{IO} + 0.3	V
VIL	Low-Level Input Voltage			-0.3	-	0.3 x V _{IO}	V
I _{IH}	High-Level Input Current		$V_{TXD} = V_{CC} = V_{IO} = 5.5 \text{ V}$	-2	0	2	μA
I _{IL}	Low-Level Input Current		$V_{TXD} = 0 \text{ V}, V_{CC} = V_{IO} = 5.5 \text{ V}$	-250	-160	-30	μA
I _{LKG_OFF}	Unpowered Leakage Current		$V_{TXD} = 5.5 \text{ V},$ $V_{CC} = V_{IO} = 5.5 \text{ V}$	-1	0	1	μA
Cı	Input Capacitance (1)			-	5	-	pF
Pin RXD,	(CAN Receive Data Output)				'		
Іон	High-Level Output Current		VRXD = VIO - 0.4 V	-8	-6	-1	mA
I _{OL}	Low-Level Output Current		V _{RXD} = 0.4 V, bus dominant	2	5	12	mA
I _{LKG_OFF}	Unpowered Leakage Current		V _{RXD} = 5.5 V, V _{CC} = V _{IO} = 0 V	-1	0	1	μΑ
Pins CAN	H and CANL, (CAN Bus Lines	()			'		
	Dawin and Bur Outsut	CANH	$V_{TXD} = 0 \text{ V}, V_{S} = 0 \text{ V}, 50 \Omega$	2.75	3.5	4.5	V
V _{O_DOM}	Dominant Bus Output Voltage	CANL	$\leq R_L \leq 65 \Omega$, $C_L = open$, $t < to(dom)TXD$	0.5	1.5	2.25	V
V _{SYM_DC}	DC Output Symmetry (dominarecessive) (V _{CC} – V _{O(CANH)} – V		$V_S = 0 \text{ V}, R_L = 60 \Omega, C_L = open,$	-0.4	-	0.84	V
Vsym	Transient Symmetry (dominal (Vo(CANH) + Vo(CANL)) / Vcc (1)		$V_S = 0 \text{ V}, R_L = 60 \Omega, C_{SPLIT}$ = 4.7 nF, C _L = open, T _{XD} = 250 kHz, 1 MHz	0.9	1.0	1.1	V/V
Vod_dom	Differential Output Voltage (de	ominant)	Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_S = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, 45 $\Omega \leq R_L < 50$ Ω , $C_L = open$	1.5	-	3	V

www.3peak.com 9 / 21 CA20230206A0



	Parameter		Conditions	Min	Тур	Max	Unit
			Normal mode, t < $t_{to(dom)TXD,VTXD} = 0 \text{ V, V}_S = 0$ $V, 4.75V \le V_{CC} \le 5.25 \text{ V, } 50$ $\Omega \le R_L < 65 \Omega, C_L = open$	1.5	-	3	V
			Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_S = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, 65 $\Omega \leq R_L \leq$ 70 Ω , $C_L = open$	1.5	-	3.3	V
			Normal mode, $t < t_{to(dom)TXD}$, $V_{TXD} = 0$ V, $V_S = 0$ V, 4.75 V $\leq V_{CC} \leq 5.25$ V, $R_L = 2240$ Ω , $C_L = open$	1.5	-	5	V
V	Differential Output Voltage (re	opposivo)	Normal mode, $V_{TXD} = V_{CC}$, $V_S = 0 V$, no load	-50	-	50	mV
V _{OD_REC}	Differential Output Voltage (re	ecessive)	Silent mode, V _{TXD} = V _S = V _{CC} , no load	-50	-	50	mV
.,			Normal mode, $V_{TXD} = V_{IO} = V_{CC}$, $V_S = 0$ V, no load	2	0.5 x VCC	3	V
V _{O_REC}	Recessive Bus Output Voltage		Silent mode, V _{TXD} = V _S = V _{IO} = V _{CC} , no load	2	0.5 x VCC	3	V
V _{CM}	Common Mode Range		V _S = 0 or V _{CC} or V _{IO}	-30	-	30	V
	Differential Receiver Threshold Voltage		Normal mode, $V_S = 0 \text{ V}$, $-30 \text{ V} \le V_{CANH} / V_{CANL} \le 30 \text{ V}$	0.5	0.7	0.9	V
V _{TH_RX_DIF}			Silent mode, $V_S = V_{IO}$, $-30 \text{ V} \le V_{CANH} / V_{CANL} \le 30$ V	0.5	0.7	0.9	V
V	Receiver Recessive Voltage,	Normal Mode	20.1/4.1/1 // 4.20.1/1	-4	-	0.5	V
V _{REC_RX}	Receiver Recessive Voltage,	Silent Mode	-30 V ≤ V _{CANH} /V _{CANL} ≤ 30 V	-4	-	0.5	V
V_{DOM_RX}	Receiver Dominant Voltage, N	Normal Mode	-30 V ≤V _{CANH} /V _{CANL} ≤ 30 V	0.9	-	9	V
▲ DOM_KX	Receiver Dominant Voltage, S	Silent Mode	JO V - V CANH/ V CANL = JU V	0.9	-	9	V
V _{HYS_RX_DI}	Differential Passiver Hystores	ris Voltago	Normal mode, −30 V ≤ V _{CANH} / V _{CANL} ≤ 30 V	50	120	200	mV
F	Differential Necesser Hysteres	Differential Receiver Hysteresis Voltage		50	120	200	mV
la ao	Dominant Short-Circuit	CANH	V _S = 0 V, V _{CANH} = -15 V to 18 V, CANL = open, V _{TXD} = 0 V	-115	-	-	mA
lo_sc_dom	Output Current CANL		$V_S = 0 \text{ V}, V_{CANL} = -15 \text{ V to}$ 18 V, CANH = open, $V_{TXD} = 0 \text{ V}$	-	-	115	mA

www.3peak.com 10 / 21 CA20230206A0



	Parameter	Conditions	Min	Тур	Max	Unit
lo_sc_rec	Recessive Short-Circuit Output Current	$-27 \text{ V} \le \text{V}_{\text{CANH}}/\text{V}_{\text{CANL}} \le 32$ V, $\text{V}_{\text{TXD}} = \text{V}_{\text{CC}},$ normal modes	-5	-	5	mA
I _{LKG_IOFF}	Power-off (unpowered) Bus Input Leakage Current	$V_{CC} = V_{IO} = 0 \text{ V or}$ $V_{CC} = V_{IO}$ shorted to ground via $47k\Omega$, $V_{CANH} = 5 \text{ V}$, $V_{CANL} = 5 \text{ V}$	-5	-	5	μΑ
R _{IN}	Input Resistance (CANH or CANL)	$V_{CC} = V_{IO} = 5 \text{ V}, V_S = 0 \text{ V},$ $-30 \text{ V} \le V_{CM} \le +30 \text{ V}$	10	23	30	kΩ
R _{IN_M}	Input Resistance Matching: [1 - R _{IN(CANH)} / R _{IN(CANL)}] × 100%	V _{CC} = V _{IO} = 5 V, V _S =0 V, V _{CANH} = V _{CANL} =5 V	-2	-	2	%
R _{ID}	Differential Input Resistance	$V_{CC} = V_{IO} = 5 \text{ V}, V_S = 0 \text{ V},$ -30 V \le V_{CM} \le +30 V	30	47	60	kΩ
Cı	Input Capacitance to Ground (CANH or CANL) (1)		-	-	20	pF
C _{ID}	Differential Input Capacitance (1)		-	-	10	pF

⁽¹⁾ The Typ data is based on bench test and design simulation.

www.3peak.com 11 / 21 CA20230206A0

⁽²⁾ Only device with V suffix (TPT1051V) have a V_{IO} Pin, device without V suffix (TPT1051) V_{IO} connected to V_{CC} internally.



AC Timing Requirements

All test conditions: V_{CC} = 4.5 V to 5.5 V, V_{IO} = 3.0 V to 5.5 V, R_L = 60 Ω , T_A = -40°C to 125°C, unless otherwise noted.

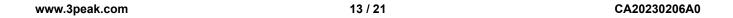
	Parameter	Conditions	Min	Тур	Max	Unit
Transceiver	Switching Characteristics					
t _{pLD}	Propagation delay time, low TXD to driver dominant (recessive to dominant) (1)		-	60	100	ns
t_{pHR}	Propagation delay time, high TXD to driver recessive (dominant to recessive) (1)	Normal mode, V _S = 0 V, R _L = 60	-	60	100	ns
t _{SK_P}	Pulse Skew (t _{pHR} - t _{pLD}) (1)	Ω , $C_L = 100 pF$	-	10	35	ns
t _R	Differential Output Signal Rise Time ⁽¹⁾		-	45	-	ns
t _F	Differential Output Signal Fall Time ⁽¹⁾			45		ns
t _{PROP_TXDL} -	Total loop delay, driver input (TXD) low to receiver output (RXD) low, recessive to dominant (1)	Normal mode, $V_S = 0 \text{ V}$, $R_L = 60$	- <	110	220	ns
t _{PROP_TXDH} -	Total loop delay, driver input (TXD) high to receiver output (RXD) high, dominant to recessive (1)	Ω , $C_L = 100$ pF, $C_{L(RXD)} = 15$ pF,	<u> </u>	140	220	ns
t _{pRH}	Propagation Delay Time, Bus Recessive Input to RXD High Output (Dominant to Recessive) ⁽¹⁾		-	90	120	ns
t _{pDL}	Propagation Delay Time, Bus Dominant Input to RXD Low Output (Recessive to Dominant) ⁽¹⁾	Vs = 0 V, C _{L(RXD)} = 15 pF	-	90	120	ns
t _{R_R}	RXD Output Signal Rise Time (1)		-	20	1	ns
t _{R_F}	RXD Output Signal Fall Time (1)		-	20	-	ns
FD Timing F	Parameters					
t _{BIT_BUS}	Bit time on CAN bus output pins with $t_{BIT_TXD} = 500 \text{ ns}^{(1)}$	W 0 W D 0 0 0 0 100 7	435	-	530	ns
	Bit time on CAN bus output pins with $t_{BIT_TXD} = 200 \text{ ns}^{(1)}$	$V_S = 0 \text{ V}, \text{ R}_L = 60 \Omega, \text{ C}_L = 100 \text{ pF},$ $C_{L(RXD)} = 15 \text{ pF}, \Delta t_{REC} = t_{BIT_RXD} - t_{REC} = t_{BIT_RX$	155	-	210	ns
t _{BIT_RXD}	Bit time on RXD output pins with t _{BIT_TXD} = 500 ns	-tвıт_в∪s	400	-	550	ns

www.3peak.com 12 / 21 CA20230206A0



	Parameter	Conditions	Min	Тур	Max	Unit
	Bit time on RXD output pins with t_{BIT_TXD} = 200 ns		120	-	220	ns
Δt_{REC}	Receiver timing symmetry with $t_{BIT_TXD} = 500 \text{ ns}^{(1)}$		-65	-	40	ns
	Receiver timing symmetry with $t_{BIT_TXD} = 200 \text{ ns}^{(1)}$		-45		15	ns
Device Timi	Device Timing Parameters					
t _{TXD_DTO}	TXD dominant time-out time	normal mode, $V_S = 0 \text{ V}$, $R_L = 60$ Ω , $C_L = \text{open}$, $V_{TXD} = 0 \text{ V}$	0.3	2	5	ms

⁽¹⁾ The test data is based on bench test and design simulation.





Detailed Description

Overview

The TPT1051 is a CAN transceiver that meets the ISO11898 high-speed CAN (Controller Area Network) physical layer standard. The device is designed to be used in CAN FD networks up to 5 Mbps, with enhanced timing margin and higher data rates in long and highly loaded networks. As designed, the device features crosswire, overvoltage, and loss of ground protection from -70 V to +70 V, over-temperature shutdown, with a -30 V to +30 V common-mode input voltage range. The TPT1051 has a secondary power supply input for I/O level shifting the input pin thresholds and RXD output level. This family has a silent mode which is also commonly referred to as the listen-only mode. Additionally, all devices include many protection features to enhance the device and network robustness.

Functional Block Diagram

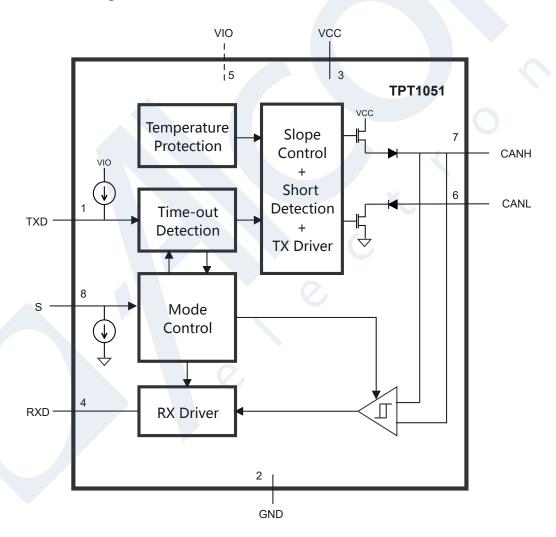


Figure 1. Functional Block Diagram

www.3peak.com 14 / 21 CA20230206A0



Feature Description

Table 3. Driver Function Table

Davies	Inp	uts	Out	Driven BUS State	
Device	S	TXD	CANH	CANL	Driven BUS State
		L	Н	L	Dominant
All Devices	L or open	H or Open	Z	Z	Recessive
	Н	Х	Z	Z	Recessive

Table 4. Receiver Function Table

Device Mode	CAN Differential Inputs V _{ID} = V _{CANH} - V _{CANL}	Bus State	RXD Terminal
	$V_{ID} \ge V_{IT+(MAX)}$	Dominant	L
Name of an Cilomb	$V_{\text{IT-(MIN)}} < V_{\text{ID}} < V_{\text{IT+(MAX)}}$	Indeterminate	Indeterminate
Normal or Silent	$V_{ID} \le V_{IT-(MIN)}$	Recessive	Н
	Open (V _{ID} ≈ 0 V)	Open	Н

Normal Mode

A low level on the S pin selects the normal mode. In this mode, the transceiver will transmit and receive data via the bus lines CANH and CANL. The differential receiver converts the analog data on the bus lines into digital data, which is output to the RXD pin. The slopes of the output signals on the bus lines are controlled internally and optimized to guarantee the lowest possibility for Electro Magnetic Emission (EME).

Silent Mode

A high level on the S pin selects the silent mode. In the silent mode, the transmitter is disabled, releasing the bus pins to the recessive state. All other IC functions, including the receiver, continue to operate as in the normal mode, just like the listen-only mode. Silent mode can be used to prevent a faulty CAN controller from disrupting all network communications.

Time-out Function in TXD Dominant Mode

When the TXD pin is set to low, the timer of 'TXD dominant time-out' is started. If the low state on TXD persists for longer than t_{TXD_DTO} , the transmitter is disabled and the bus lines are in recessive state. This function prevents a hardware and/or software application failure from driving the bus lines to a permanent dominant state which will block all network communications. The TXD dominant time-out timer is reset as TXD is pulled to high. The TXD dominant time-out time also defines that the data rate should be faster than 10 kbit/s.

Time-out Function in Bus Dominant Mode

In the silent mode, the timer of 'bus dominant time-out' is started when the CAN bus changes from recessive to dominant state. If the dominant state on the bus persists for longer than the t_{BUS_DTO}bus, the RXD pin is reset to high. If a bus short-circuits or a failure in one of the other nodes on the network, this function prevents a clamped dominant bus from generating a permanent wake-up request. The bus dominant time-out timer is reset when the CAN bus changes from a dominant to recessive state.

www.3peak.com 15 / 21 CA20230206A0



Over-Temperature Protection (OTP)

The output drivers are protected against over-temperature conditions. If the virtual junction temperature exceeds the shutdown junction temperature T_{OTP} , the output drivers will be disabled until the virtual junction temperature falls below T_{OTP} and TXD becomes recessive again. Including the TXD condition to ensures output driver oscillation due to temperature drift is avoided.



www.3peak.com 16 / 21 CA20230206A0



Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

The TPT1051 is a CAN transceiver to support CAN FD function up to 5 Mbps, with BUS protection voltage from –70 V to +70 V, overtemperature shutdown, a –30 V to +30 V common-mode range. The VIO of TPT1051 can support the voltage level of TXD and RXD from 2.8 V to 5.5 V. The following sections show a typical application of the TPT1051.

Typical Application

Figure xx shows the typical application schematic of the TPT1051.

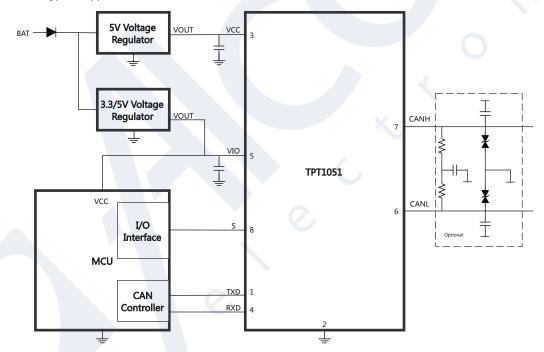
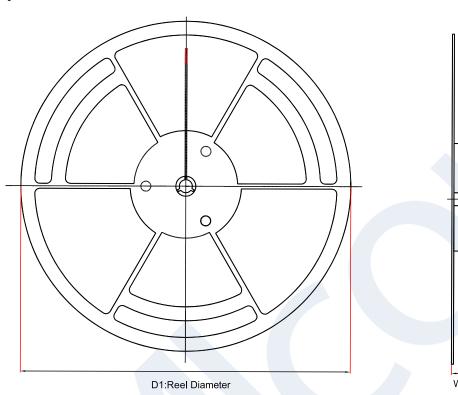


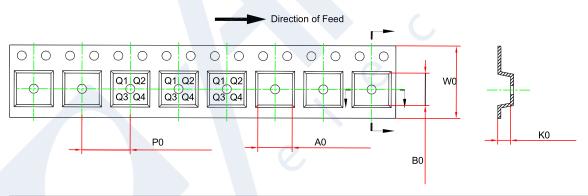
Figure 2. Typical Application Circuit

www.3peak.com 17 / 21 CA20230206A0



Tape and Reel Information



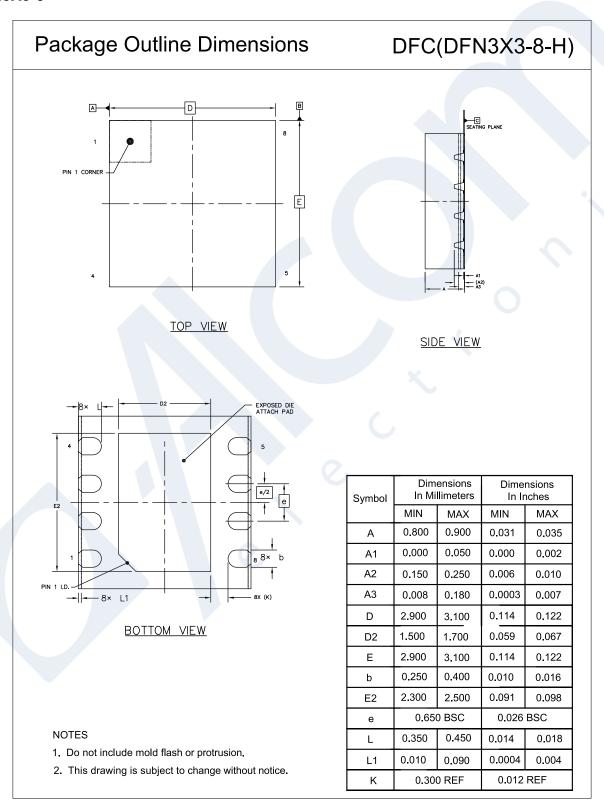


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPT1051VQ- SO1R-S	SOP8	330	17.6	6.5	5.4	2.0	8.0	12.0	Q1
TPT1051VQ- DF6R-S	DFN3x3-8	330	17.6	3.3	3.3	1.1	8.0	12.0	Q1
TPT1051Q- SO1R-S	SOP8	330	17.6	6.5	5.4	2.0	8.0	12.0	Q1
TPT1051Q- DF6R-S	DFN3x3-8	330	17.6	3.3	3.3	1.1	8.0	12.0	Q1



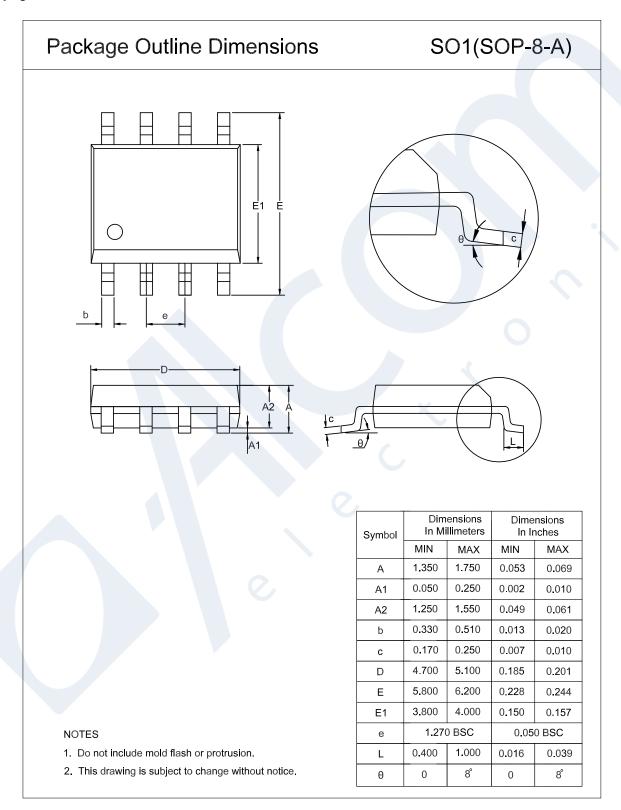
Package Outline Dimensions

DFN3X3-8





SOP-8





Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPT1051VQ-SO1R-S	−40 to 125°C	SOP8	T51VQ	MSL1	Tape and Reel, 4000	Green
TPT1051VQ-DF6R-S	−40 to 125°C	DFN3x3-8	T51VQ	MSL1	Tape and Reel, 4000	Green
TPT1051Q-SO1R-S	−40 to 125°C	SOP-8	1051Q	MSL1	Tape and Reel, 4000	Green
TPT1051Q-DF6R-S	-40 to 125°C	DFN3x3-8	1051Q	MSL1	Tape and Reel, 4000	Green

^{(1) 3}PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

3PEAK and the 3PEAK logo are registered trademarks of 3PEAK INCORPORATED. All other trademarks are the property of their respective owners.

www.3peak.com 21 / 21 CA20230206A0

⁽²⁾ MSL will be updated depending on the qualification report.



This page intentionally left blank