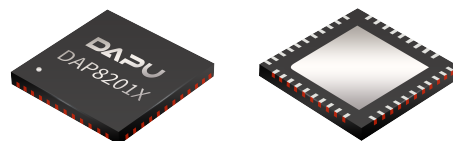


Ethernet Transceiver - DAP8201M(I)

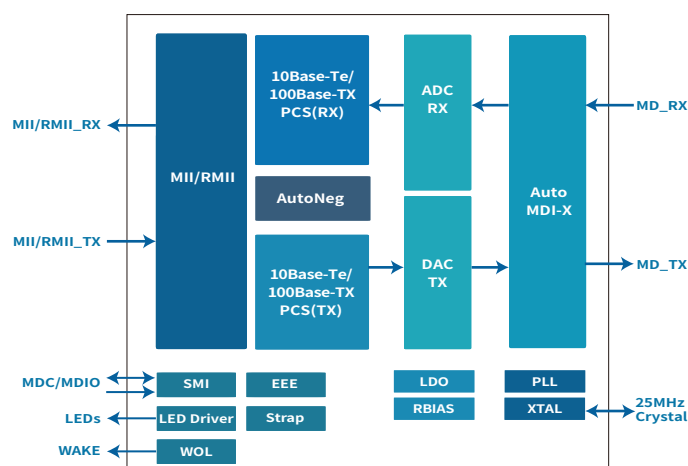
This Ethernet PHY Chip is an 100M Ethernet physical layer transceiver, which modulates and demodulates the electronic signal. It is the channel for the Ethernet MAC chip to send and receive data.

Key Features

- MAC interface: MII&RMII
- IEEE 802.3az, EEE
- 100BASE-TX IEEE 802.3u/10BASE-T IEEE 802.3 Compliant
- Full/Half duplex
- Auto-negotiation
- Power down mode
- Base Line Wander (BLW) compensation
- Auto MDIX
- Interrupt function
- WOL
- Automatic Polarity correction
- 2 sets LED indicator
- 25MHz external crystal/oscillator
- Output 50Mhz clock source for MAC
- Single Power supply, internal LDO
- Operation Temperature Range: 0°C ~ +70°C/-40°C ~ +85°C
- Package: QFN 32-pin (5mm x 5mm)



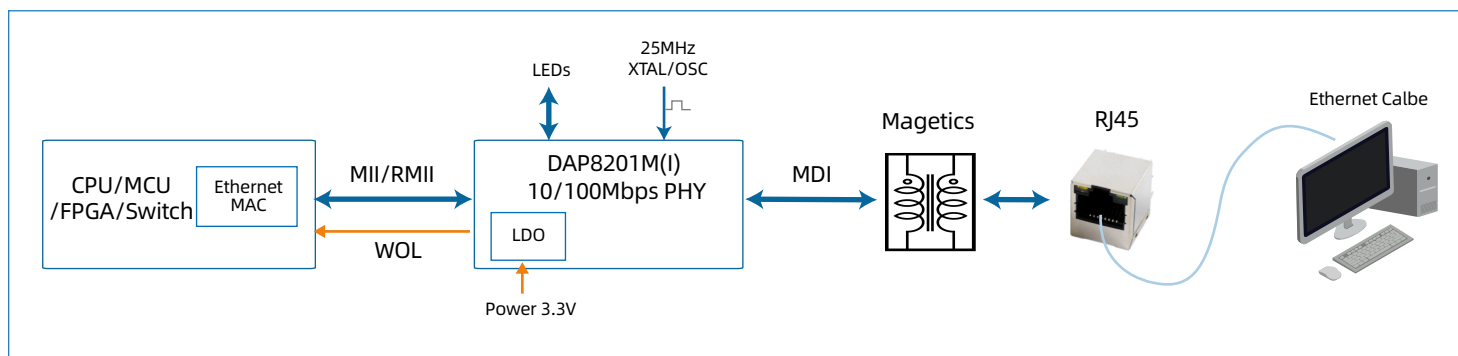
Architecture



Application

- Enterprise & SOHO
- Wireless Router
- LED Display
- Motor drives
- Industrial factory automation
- Field Bus Support
- Industrial controls and automation
- Industrial embedded computing
- Wired and wireless communications infrastructure
- Test and measurement
- Consumer electronics

Application Diagram



Product Number

PN	MII/RMII to Copper	1000Base-TX	10Base-T _e	WoL	EEE	Operation Temp.	Integrated Regulator	Package
DAP8201M	●	●	●	●	●	0°C ~ +70°C	LDO	QFN32
DAP8201MI	●	●	●	●	●	-40°C ~ +85°C	LDO	QFN32

Mechanical Dimensions

